TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

# TLP280,TLP280-4

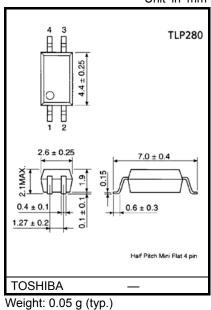
Programmable Controllers AC/DC–Input Module PC Card Modem (PCMCIA)

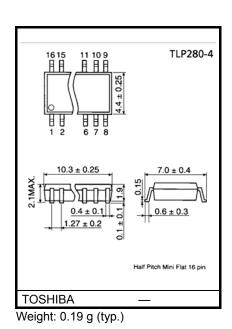
TLP280 and TLP280–4 is a very small and thin coupler, suitable for surface mount assembly in applications such as PCMCIA fax modem, programmable controllers.

TLP280 and TLP280-4 consist of photo transistor, optically coupled to two gallium arsenide infrared emitting diode connected inverse parallel, and can operate directly by AC input current

- Collector-emitter voltage: 80 V (min)
- Current transfer ratio: 50% (min) Rank GB: 100% (min)
- Isolation voltage: 2500 Vrms (min)
- UL recognized: UL1577, file No. E67349
- BSI approved: BS EN 60065: 2002, BS EN 60950-1: 2002

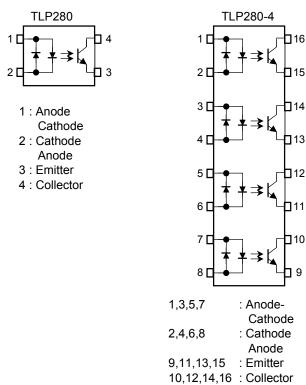
Certificate No. 8143, 8144





Unit in mm

### Pin Configuration (top view)



Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rat	Unit		
		Symbol	TLP280	TLP280-4	Unit	
Δ	Forward current	I <sub>F(RMS)</sub>	±ť	50	mA	
	Forward current derating	ΔI <sub>F</sub> /°C	–0.7 (Ta ≥ 53°C)	–0.5 (Ta ≥ 25°C)	mA /°C	
LED	Pulse forward current	I <sub>FP</sub>	±1 (100µs pulse, 100pps)		А	
	Junction temperature	Tj	12	125		
	Collector-emitter voltage	V <sub>CEO</sub>	8	80		
	Emitter-collector voltage	V <sub>ECO</sub>	7	7		
5	Collector current	Ι <sub>C</sub>	50		mA	
Detector	Collector power dissipation (1 circuit)	P <sub>C</sub>	150	100	mW	
	Collector power dissipation derating (Ta $\ge$ 25°C) (1 circuit)	∆P <sub>C</sub> /°C	-1.5	-1.0	mW /°C	
	Junction temperature	Tj	12	125		
Storage temperature range		T <sub>stg</sub>	-55~125		°C	
Operating temperature range		T <sub>opr</sub>	-55~100		°C	
Lead soldering temperature		T <sub>sol</sub>	260 (10s)		°C	
Total package power dissipation (1 circuit)		PT	200	200 170		
	al package power dissipation ating (Ta ≥ 25°C) (1 circuit)	ΔP <sub>T</sub> /°C	-2.0 -1.7		mW /°C	
Isol	ation voltage (Note)	BVS	2500 (AC, 1min., R.H.≤ 60%)		Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note): Device considered a two terminal device: LED side pins shorted together and detector side pins shorted together.

#### Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур	Max	Unit
Q	Forward voltage	VF	I <sub>F</sub> = ±10 mA	1.0	1.15	1.3	V
LED	Capacitance	CT	V = 0, f = 1 MHz	-	60	_	pF
	Collector–emitter breakdown voltage	V <sub>(BR)</sub> CEO	I <sub>C</sub> = 0.5 mA	80	_	_	V
Detector	Emitter-collector breakdown voltage	V <sub>(BR) ECO</sub>	I <sub>E</sub> = 0.1 mA	7	_	_	V
	Collector dark current	1050	V <sub>CE</sub> = 48 V, Ambient light below (100 1x)	_	0.01 (2)	0.1 (10)	μA
	(Note 1)	ICEO	V <sub>CE</sub> = 48 V, Ta = 85°C Ambient light below (100 1x)	_	2 (4)	50 (50)	μA
	Capacitance (collector to emitter)	C <sub>CE</sub>	V = 0, f = 1 MHz	_	10	_	pF

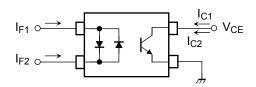
(Note 1): Because of the construction, leak current might be increased by ambient light. Please use photocoupler with less ambient light.

#### **Coupled Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Mln	Тур.	Max	Unit
Current transfer ratio	I <sub>C</sub> / I <sub>F</sub>	$I_F = \pm 5$ mA, $V_{CE} = 5$ V Rank GB	50	_	600	%
			100		600	
Saturated CTR	I <sub>C</sub> / I <sub>F (sat)</sub>	IF = $\pm 1$ mA, V <sub>CE</sub> = 0.4 V Rank GB	_	60	_	%
			30			
	V <sub>CE (sat)</sub>	I <sub>C</sub> = 2.4 mA, I <sub>F</sub> = ±8 mA	_	_	0.4	V
Collector–emitter saturation voltage		$I_{C}$ = 0.2 mA, $I_{F}$ = ±1 mA Rank GB	_	0.2	_	
5			-	_	0.4	
Off-state collector current	I <sub>C(off)</sub>	$V_{F}$ = ± 0.7 V, $V_{CE}$ = 48 V	_	_	10	μA
CTR symmetry	I <sub>C (ratio)</sub>	$I_{C} (I_{F} = -5 \text{ mA}) / I_{C} (I_{F} = 5 \text{ mA})$ (Note 2)	0.33	_	3	—

(Note 2):

	$\frac{I_{C2}(I_F = I_{F2}, V_{CE} = 5V)}{I_{C1}(I_F = I_{F1}, V_{CE} = 5V)}$
C(ratio) -	$I_{C1}(I_F = I_{F1}, V_{CE} = 5V)$



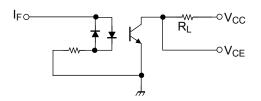
Isolation Characteristics (Ta = 25°C)

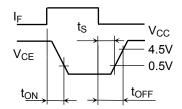
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	CS	V <sub>S</sub> = 0V, f = 1 MHz	—	0.8	—	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H.≤ 60%	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
		AC, 1 minute	2500	_	_	V
Isolation voltage	BVS	AC, 1 second, in oil	_	5000	_	V <sub>rms</sub>
		DC, 1 minute, in oil	—	5000	_	V <sub>dc</sub>

### Switching Characteristics (Ta = 25°C)

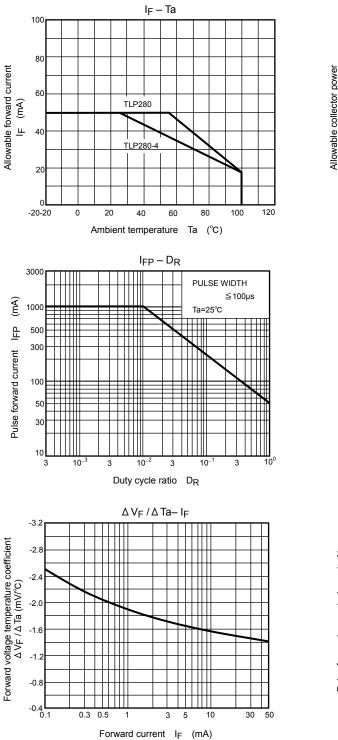
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	tr		_	2	—	μs
Fall time	t <sub>f</sub>	$V_{CC}$ = 10 V, I <sub>C</sub> = 2 mA R <sub>L</sub> = 100Ω	_	3	_	
Turn–on time	t <sub>on</sub>		_	3	_	
Turn-off time	t <sub>off</sub>		_	3	_	
Turn–on time	t <sub>ON</sub>		_	2	_	
Storage time	ts	$R_L = 1.9 kΩ$ (Fig.1) V <sub>CC</sub> = 5 V, I <sub>F</sub> = ±16 mA	_	25	_	μs
Turn-off time	tOFF		_	40	_	

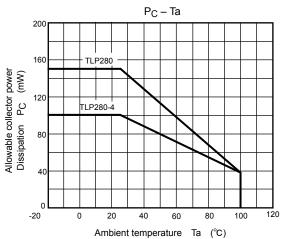
(Fig. 1): Switching time test circuit

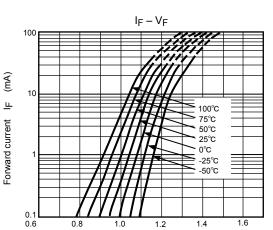




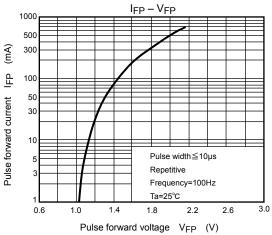
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Forward voltage V<sub>F</sub> (V)



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50

40

30

20

10

5

IF = 2mA

241/

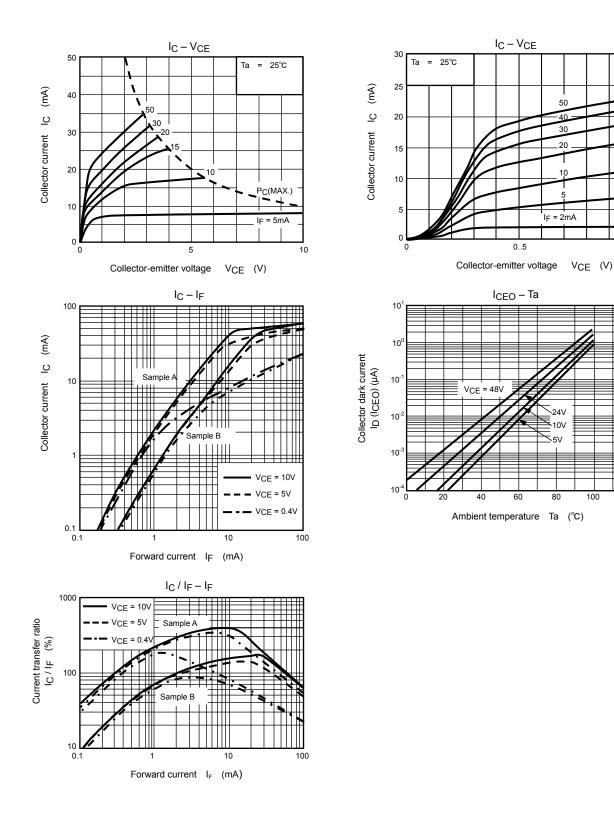
10V

5\/

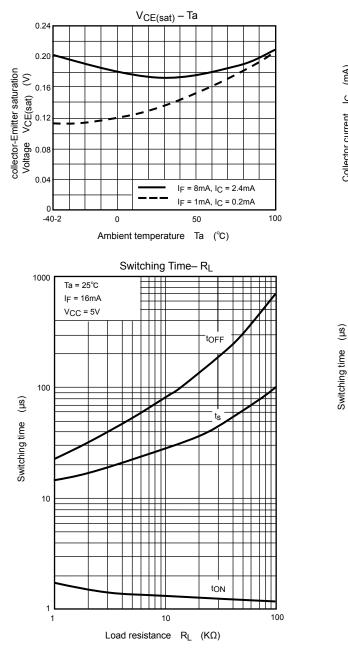
80

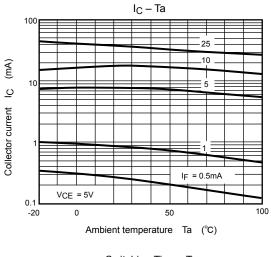
100

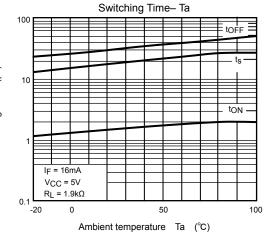
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