### SN5408, SN54LS08, SN54S08 SN7408, SN74LS08, SN74S08 QUADRUPLE 2-INPUT POSITIVE-AND GATES SDLS033 – DECEMBER 1983 – REVISED MARCH 1988

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

### description

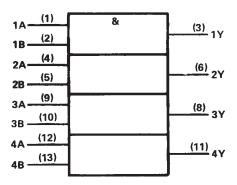
These devices contain four independent 2-input AND gates.

The SN5408, SN54LS08, and SN54S08 are characterized for operation over the full military temperature range of -55 °C to 125 °C. The SN7408, SN74LS08 and SN74S08 are characterized for operation from 0° to 70 °C.



INP	UTS	OUTPUT
A	в	Y
н	н	н
L	х	L
X	L	L.

logic symbol<sup>†</sup>



<sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

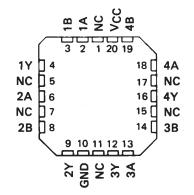
Pin numbers shown are for D, J, N, and W packages.

SN5408, SN54LS08, SN54S08 . . . J OR W PACKAGE SN7408 . . . J OR N PACKAGE SN74LS08, SN74S08 . . . D, J OR N PACKAGE

### (TOP VJEW)

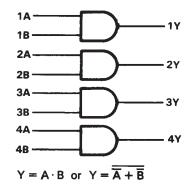
1A 1B 1Y 2A 2B	1 2 3 4 5	14 VCC 13 4B 12 4A 11 4Y 10 3B
	4	Г
2B 🗌		10 3B
2Y 🗋	6	9 🗍 <b>3A</b>
	7	8 <b>]] 3Y</b>

SN54LS08, SN54S08 . . . FK PACKAGE (TOP VIEW)



NC-No internal connection

logic diagram (positive logic)

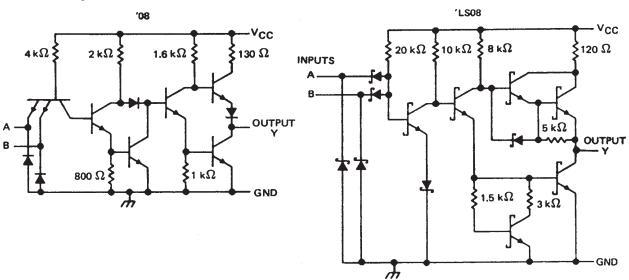


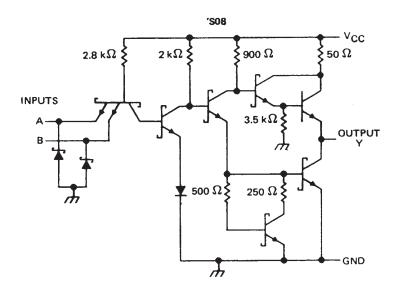


### SN5408, SN54LS08, SN54S08 SN7408, SN74LS08, SN74S08 **QUADRUPLE 2-INPUT POSITIVE-AND GATES**

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### schematics (each gate)





Resistor values are nominal.

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1) .		
Input voltage: '08, 'SO8		5.5 V
Operating free-air temperature range	: SN54'	
	SN74'	0°C to 70°C
Storage temperature range		

NOTE 1: Voltage values are with respect to network ground terminal.



# SN5408, SN54LS08, SN54S08 SN7408, SN74LS08, SN74S08 QUADRUPLE 2-INPUT POSITIVE-AND GATES SDLS033 – DECEMBER 1983 – REVISED MARCH 1988

### recommended operating conditions

		SN5408	3		SN7408	3	
	MIN	NOM	MAX	MIN	NOM	мах	UNIT
V <sub>CC</sub> Supply voltage	4.5	5	5.5	4.75	5	5.25	v
VIH High-level input voltage	2			2			v
VIL Low-level input voltage			0.8			0.8	v
IOH High-level output current			- 0.8			- 0.8	mA
IOL Low-level output current			16			16	mA
T <sub>A</sub> Operating free-air temperature	- 55		125	0		70	°c

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	· · · · · · · · · · · · · · · · · · ·		SN540	3		SN740	8	
PARAMETER	TEST CONDITIONS T	MIN	TYP‡	MAX	MIN	түр‡	МАХ	UNIT
VIK	V <sub>CC</sub> = MIN, I <sub>t</sub> = - 12 mA			- 1.5			- 1.5	V
∨он	$V_{CC} = MIN, V_{1H} = 2V, I_{OH} = -0.8 \text{ mA}$	2.4	3.4		2.4	3.4		.V.
VOL	V <sub>CC</sub> = MIN, V <sub>IL</sub> = 0.8 V, I <sub>OL</sub> = 16 mA		0.2	0.4		0.2	0.4	v
lį	V <sub>CC</sub> = MAX, V <sub>I</sub> = 5.5 V			1			1	mA
Чн	V <sub>CC</sub> = MAX, V <sub>1</sub> = 2.4 V			40			40	μA
μL	V <sub>CC</sub> = MAX, V <sub>1</sub> = 0.4 V			- 1.6			- 1.6	mA
IOS §	V <sub>CC</sub> = MAX	- 20		- 55	- 18		- 55	mA
ICCH	V <sub>CC</sub> = MAX, V <sub>I</sub> = 4.5 V		11	21		11	21	mA
ICCL	V <sub>CC</sub> = MAX, V <sub>l</sub> = 0 V		20	33		20	33	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at  $V_{CC} = 5 V$ ,  $T_A = 25^{\circ}C$ . § Not more than one output should be shorted at a time.

## switching characteristics, $V_{CC} = 5 V$ , $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	ТҮР	MAX	UNIT
<sup>t</sup> PLH					17.5	27	ns
tPHL	A or B	Y	R <sub>L</sub> = 400 Ω, C <sub>L</sub> = 15 pF		12	19	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



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## SN5408, SN54LS08, SN54S08 SN7408, SN74LS08, SN74S08 **QUADRUPLE 2-INPUT POSITIVE-AND GATES**

SDLS033 – DECEMBER 1983 – REVISED MARCH 1988

### recommended operating conditions

		SN54LS	80		SN74LS	806	UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
V <sub>CC</sub> Supply voltage	4.5	5	5.5	4.75	5	5.25	v
VIH High-level input voltage	2			2			v
VIL Low-level input voltage			0.7			0.8	v
IOH High-level output current			- 0.4			- 0.4	mA
IOL Low-level output current			4			8	mA
T <sub>A</sub> Operating free-air temperature	- 55		125	0		70	°C

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

		TEST CONDITIONS T			SN64LS	08		SN74LS	08	
PARAMETER				MIN	TYP‡	MAX	MIN	TYP‡	мах	UNIT
VIK	V <sub>CC</sub> = MIN,	lı = – 18 mA				- 1.5			- 1.5	V
VOH	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	<sup>I</sup> OH = - 0.4 mA	2.5	3.4		2.7	3.4		v
N	V <sub>CC</sub> = MIN,	V <sub>IL</sub> = MAX,	I <sub>OL</sub> = 4 mA		0.25	0.4		0.25	0.4	v
VOL	V <sub>CC</sub> = MIN,	VIL = MAX,	IOL = 8 mA					0.35	0.5	
1	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 7 V				0.1			0.1	mA
ін	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 2.7 V				20			20	μA
կլ	V <sub>CC</sub> = MAX,	V1 = 0.4 V				- 0.4			- 0.4	mA
los§	V <sub>CC</sub> = MAX			- 20		- 100	- 20		- 100	mA
Іссн	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 4.5 V			2.4	4.8		2.4	4,8	mA
ICCL	V <sub>CC</sub> = MAX,	V1 = 0 V			4.4	8.8		4.4	8.8	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at  $V_{CC} = 5 V$ ,  $T_A = 25^{\circ}C$ § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

### switching characteristics, $V_{CC} = 5 V$ , $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	MIN	түр	мах	UNIT	
<sup>t</sup> PLH	A or B	×	R <sub>L</sub> = 2 kΩ,	R <sub>L</sub> = 2 kΩ, C <sub>L</sub> = 15 pF		8	15	ns
<sup>t</sup> PHL	AOIB	Ŧ				10	20	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



## SN5408, SN54LS08, SN54S08 SN7408, SN74LS08, SN74S08 **QUADRUPLE 2-INPUT POSITIVE-AND GATES** SDLS033 – DECEMBER 1983 – REVISED MARCH 1988

### recommended operating conditions

			SN54S0	8		SN74S0	8	UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
V <sub>CC</sub> Su	ipply voltage	4.5	5	5.5	4.75	5	5.25	v
VIH Hi	gh-level input voltage	2			2			v
VIL LO	ow-level input voltage			0.8		_	0.8	v
IOH Hi	igh-level output current			- 1		_	- 1	mA
IOL LO	ow-level output current			20			20	mA
TA O	perating free-air temperature	- 55		125	0		70	°c

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

					SN54S0	8		SN74S0	8	UNIT
PARAMETER		TEST CONDIT	TIONS T	MIN	TYP‡	MAX	MIN	TYP‡	MAX	
VIK	V <sub>CC</sub> = MIN,	l <sub>1</sub> = -18 mA				-1.2			-1.2	v
VOH	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	IOH = - 1 mA	2.5	3.4		2.7	3.4		v
VOL	V <sub>CC</sub> = MIN,	V <sub>IL</sub> = 0.8 V	1 <sub>OL</sub> = 20 mA			0.5			0.5	v
l <sub>l</sub>	V <sub>CC</sub> = MAX,	VI ≈ 5.5 V				1			1	mA
ін	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 2.7 V				50			50	μA
μL	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 0.5 V				-2			2	mA
los§	V <sub>CC</sub> = MAX			-40		-100	-40		100	mA
ICCH	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 4.5 V	<u> </u>		18	32		18	32	mA
ICCL	V <sub>CC</sub> = MAX,	VI = 0 V			32	57		32	57	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at  $V_{CC} = 5 V$ ,  $T_A = 25^{\circ}C$ . § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

PARAMETER	FROM (INPUT)	то (оитрит)	TEST CONDITIONS	MIN	түр	MAX	UNIT
<sup>t</sup> PLH			R <sub>I</sub> = 280 Ω, C <sub>L</sub> = 15 pF		4.5	7	ns
<sup>t</sup> PHL		v	HL-20032, CE-130		5	7,5	ns
<sup>t</sup> PLH	A or B	A or B Y	$R_1 = 280 \Omega$ , $C_1 = 50 \rho F$		6		ns
<sup>t</sup> PHL			R <sub>L</sub> = 280 Ω, C <sub>L</sub> = 50 ρF		7,5		ns

### switching characteristics, $V_{CC} = 5 V$ , $T_A = 25^{\circ}C$ (see note 2)

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



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